

REMARKS

Claims 1-28 and 32-39 are pending in this application. By this Amendment, claims 1, 11, 15-16, 19, and 36-37 are amended. No new matter is added. Reconsideration in view of the above amendments and the following remarks is respectfully requested.

Applicant gratefully acknowledges that the Office Action indicates that claims 3-4, 17-18, 21, and 23 contain allowable subject matter, and that claim 36 is allowed. However, for at least the reasons described below, Applicant respectfully submits that all claims 1-28 and 32-39 contain allowable subject matter.

I. 35 U.S.C. §112, Second Paragraph, Rejection

The Office Action rejects claims 15-16 under 35 U.S.C. §112, second paragraph, because of a lack of sufficient antecedent basis. Claim 15 is amended to recite "a data signal," and claim 16 is amended to recite "the first transistor," so as to obviate the rejection. Accordingly, Applicant respectfully requests that the Examiner withdraw the §112, second paragraph, rejection.

II. Claims Define Patentable Subject Matter

The Office Action rejects claims 1-2, 5-14, 19-20, 22, 24-27, 32-35, and 37-39 under 35 U.S.C. §102(e) as being anticipated by Akimoto (U.S. Patent Application Publication No. 2003/0067424); rejects claims 15-16 under 35 U.S.C. 102(b) as being anticipated by Dawson (U.S. Patent No. 6,229,506); and rejects claim 28 under 35 U.S.C. §103(a) as being unpatentable over Akimoto in view of Matsumoto (U.S. Patent Application Publication No. 2002/0167504). Applicant respectfully traverses these rejections.

A. Independent Claims 1, 11, 19, and 36

Specifically, Applicant respectfully asserts that Akimoto, Dawson, and Matsumoto, individually or in combination, fail to disclose an electronic circuit including at least a control circuit and a second transistor having a fourth terminal being directly connected to the first

power source line, as recited in independent claims 1, 11, 19, and allowed claim 36. The Office Action, in the first paragraph on page 15 indicates that none of the references, singularly or in combination, disclose the aforementioned feature recited in claims 1, 11, 19, and 36.

B. Independent Claims 15 and 37

Regarding the §102(b) rejection of claims 15-16, Applicant respectfully asserts that Dawson, Akimoto, and Matsumoto, individually or in combination, fail to disclose an electronic circuit or an electro-optical device including at least a first potential of the first electrode being set to a first voltage by electrically connecting the first electrode and the power source line through the second transistor during a first period, and a second potential of the first control terminal being set to a second voltage by a data signal that is supplied to the capacitive element through the third transistor during a second period and that changes the first potential from the first voltage by a capacitance coupling involved with the capacitive element, as recited in claims 15 and 37.

Dawson, discloses a load data phase and a continuous illuminating phase, where data is stored on the storage capacitor Cs during the load data phase, and where the gate voltage on the transistor will track with its source voltage such that the source-to-gate voltage is maintained the same throughout the entire load and illumination phases. Dawson, however, fails to disclose a second potential of the first control terminal being set to a second voltage by a data signal that is supplied to the capacitive element through the third transistor during a second period and that changes the first potential from the first voltage by a capacitance coupling involved with the capacitive element, as recited in claims 15 and 37.

Akimoto and Matsumoto also fail to disclose the above feature recited in claims 15 and 37, and therefore, fail to make up for the deficiencies of Dawson.

Thus, Dawson, Akimoto, and Matsumoto, individually or in combination, fail to

disclose all of the features of claims 15 and 37.

III. Conclusion

In accordance with the above remarks, Applicant submits that independent claims 1, 11, 15, 19, 36, and 37 define patentable subject matter. Claims 2-10, 12-14, 16-18, 20-28, 32-35, and 38-39 depend from claims 1, 11, 15, and 19, respectively, and therefore, also define patentable subject matter, as well as for the additional features they recite. Thus, Applicants respectfully request that the Examiner withdraw the rejections.

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance of claims 1-28 and 32-39 are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,



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Attachment:

Request for Continued Examination

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